https://newtoncolmore.com/job/senior-fpga-engineer-hpc-hardware-design/

Senior FPGA Engineer – HPC Hardware Design

Description
Newton Colmore is seeking a Senior FPGA Engineer to join a small hardware engineering team building systems that operate at the limits of speed and throughput.

In this role you will be designing and optimising FPGA-based systems for low-latency, high-throughput data processing, working on custom hardware acceleration, high-speed data pipelines, and performance-critical infrastructure where every nanosecond counts.

This is a multidisciplinary team that is tight-knit and free of hierarchy, where innovation and inventive thought is at the heart of everything they do. The role will involve hands-on simple circuitry design as well as tackling highly complex overarching technical challenges.

To be successful in this role we are looking for engineers with strong FPGA design experience, ideally in low-latency, high-throughput or real-time environments. The client is considering candidates from HPC, defence, telecoms, or trading, and is looking for a candidate that has a deep understanding of digital design, timing closure and hardware/software co-design.

In the pursuit of hiring great people, the company are offering bespoke packages that can be tailored to suit, plus monetary bonuses and a full suite of other benefits. They have built an environment that welcomes diverse thinking and they are looking for engineers at all levels of seniority to help with their mission.

For a confidential discussion, please apply or contact us directly. Full details of the company and technology will be shared with shortlisted candidates.

Hiring organization
Newton Colmore Consulting Ltd

Employment Type
Full-time

Beginning of employment
ASAP

Duration of employment
Permanent

Industry
High Performance Computing

Job Location
London

Working Hours
Monday to Friday

Date posted
6th October 2026

Valid through
23.11.2026

Apply below or email directly: matthew.lowdon@newtoncolmore.com